



ATTORNEY'S DOCKET NO.: 1466-US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.: 10/008,967 Art Unit 2116  
Applicants: Robert Bruce Gage et al. Examiner: A. I. Elamin  
Filed: November 9, 2001 Conf. No. 3775  
Title: CLOCK ARCHITECTURE FOR A FREQUENCY-BASED TESTER

CERTIFICATE OF MAILING

I hereby certify that this correspondence along with all papers or documents referred to therein as being attached or enclosed is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on April 26, 2005.

Kathryn E. Noll  
Kathryn E. Noll

MAIL STOP AMENDMENT  
COMMISSIONER FOR PATENTS  
P.O. BOX 1450  
ALEXANDRIA, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**

Sir:

Pursuant to 37 CFR §§1.56, 1.97 and 1.98, enclosed is Form PTO-1449 citing references submitted for consideration by the Examiner. A copy of each cited foreign patent or publication other than US patents and US patent application publications is also enclosed.

This Information Disclosure Statement is being filed before the mailing of a notice of allowance in the above-identified application. The fee set forth in CFR § 1.17(p) is enclosed.

A copy of the Search Report from a foreign counterpart application is enclosed.

Please charge the \$180 fee and any additional fees to Deposit Account No. 20-0515, Docket No. 1466.

Respectfully submitted,

Kathryn E. Noll

Kathryn E. Noll  
Reg. No.: 48,811  
Attorney for Applicants

DATE: April 26, 2005

Teradyne, Inc.  
321 Harrison Avenue, MS-H61  
Boston, MA 02118  
Telephone: (617) 422-2676  
Facsimile: (617) 422-2290

04/29/2005 SFELEKE1 00000053 200515 10088967  
01 FC:1896 180.00 DA



Information Disclosure Statement by Applicant  (Use as many sheets as necessary)		Application No.	10/008,967
		Filing Date	November 9, 2001
		First Named Inventor	Robert Bruce Gage
		Group Art Unit	2116
		Examiner Name	A. I. Elamin
		Attorney's Docket No.	1466-US
Sheet 1 of 1			

U.S. Patent Documents					
Examiner Initials	Cite No.	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code (if known)			
A1	US-5,153,883		10-06-1992	Hayashi et al.	
A2	US-5,710,517		01-20-1998	Meyer	
A3	US-5,712,883		01-27-1998	Miller et al.	
A4	US-5,917,834		06-29-1999	Arkin	
A5	US-5,935,256		08-10-1999	Lesmeister	
A6	US-6,028,439		02-22-2000	Arkin et al.	
A7	US-6,121,816		09-19-2000	Tonks et al.	
A8	US-6,130,552		10-10-2000	Jefferson et al.	
A9	US-6,188,253 B1		02-13-2001	Gage et al.	
A10	US-6,208,702 B1		03-27-2001	Ghoshal	
A11					
A12					
A13					

Foreign Patent Documents or Published Foreign Patent Applications						
Examiner Initials	Cite No.	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	Translation
						Yes No
B1						
B2						
B3						
B4						

Non Patent Literature Documents (include author, title, date, pages, publisher and place of publication)		
Examiner Initials	Cite No.	Document
	C1	L. Ashby, ASIC CLOCK DISTRIBUTION USING A PHASE LOCKED LOOP (PLL), Sept. 1991, pp. P1-6.1-6.3, IEEE, New York, NY.
	C2	

Examiner Signature	Date Considered
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	